PAAVAI ENGINEERING COLLEGE, NAMAKKAL – 637018
(AUTONOMOUS)
REGULATIONS 2019
CHOICE BASED CREDIT SYSTEM
B.TECH. – INFORMATION TECHNOLOGY

CURRICULUM

(Applicable to the candidates admitted during the academic year 2020-2021 onwards)

SEMESTER III

S. No	Category	Course Code	Course Title	L	T	P	C
Theory							
1	HS	GE20301	தமிழரும் தொழில்நுட்பமும்/Tamils and Technology	1	0	0	1
2	BS	MA20301	Transforms and Boundary Value Problems	3	1	0	4
3	ES	EC20308	Digital Principles and System Design	3	0	0	3
4	PC	IT20301	Object Oriented Programming	3	0	0	3
5	PC	IT20302	Design and Analysis of Algorithms	3	0	0	3
6	PC	IT20303	Computer Architecture	3	0	0	3
7	MC	MC20301	Value Education	2	0	0	0
Practical							
8	ES	EC20309	Digital Laboratory	0	0	4	2
9	PC	IT20306	Object Oriented Programming Laboratory	0	0	4	2
10	EE	EN20301	English Proficiency Course Laboratory	0	0	2	1
			Total	**18**	**01**	**10**	**22**

SEMESTER IV

S. No	Category	Course Code	Course Title	L	T	P	C
Theory							
1	BS	MA20403	Probability and Statistics	3	1	0	4
2	ES	EC20407	Microprocessors and Microcontrollers	3	0	0	3
3	PC	IT20401	Software Engineering	3	0	0	3
4	PC	IT20402	Python Programming	3	0	0	3
5	PC	IT20403	Operating Systems	3	0	0	3
Practical							
7	ES	EC20408	Microprocessors and Microcontrollers Laboratory	0	0	4	2
8	PC	IT20405	Python Programming Laboratory	0	0	4	2
9	PC	IT20406	Operating Systems Laboratory	0	0	4	2
			Total	**15**	**01**	**12**	**22**

PAAVAI ENGINEERING COLLEGE, NAMAKKAL
Approved
BOARD OF STUDIES
Information Technology
28/17
AUTONOMOUS

GE20301	TAMILS AND TECHNOLOGY	L	T	P	C
		1	0	0	1

UNIT I WEAVING AND CERAMIC TECHNOLOGY 3

Weaving Industry during Sangam Age – Ceramic technology – Black and Red Ware Potteries (BRW) – Graffiti on Potteries.

UNIT II DESIGN AND CONSTRUCTION TECHNOLOGY 3

Designing and Structural construction House & Designs in household materials during Sangam Age - Building materials and Hero stones of Sangam age – Details of Stage Constructions in Silappathikaram - Sculptures and Temples of Mamallapuram - Great Temples of Cholas and other worship places - Temples of Nayaka Period - Type study (Madurai Meenakshi Temple)- Thirumalai Nayakar Mahal - Chetti Nadu Houses, Indo - Saracenic architecture at Madras during British Period.

UNIT III MANUFACTURING TECHNOLOGY 3

Art of Ship Building - Metallurgical studies - Iron industry - Iron smelting,steel -Copper and goldCoins as source of history - Minting of Coins – Beads making-industries Stone beads -Glass beads - Terracotta beads -Shell beads/ bone beats - Archeological evidences - Gem stone types described in Silappathikaram.

UNIT IV AGRICULTURE AND IRRIGATION TECHNOLOGY 3

Dam, Tank, ponds, Sluice, Significance of Kumizhi Thoompu of Chola Period, Animal Husbandry - Wells designed for cattle use - Agriculture and Agro Processing - Knowledge of Sea - Fisheries – Pearl - Conche diving - Ancient Knowledge of Ocean - Knowledge Specific Society.

UNIT V SCIENTIFIC TAMIL & TAMIL COMPUTING 3

Development of Scientific Tamil - Tamil computing – Digitalization of Tamil Books – Development of Tamil Software – Tamil Virtual Academy – Tamil Digital Library – Online Tamil Dictionaries – Sorkuvai Project

TOTAL PERIODS: 15

TEXT CUM REFERENCE BOOKS:

1. தமிழக வரலாறு – மக்களும் பண்பாடும் – கே.கே.பிள்ளை. (வெளியீடு தமிழ்நாடு பாடநூல் மற்றும் கல்வியியல் பணிகள் கழகம்).
2. கணினித் தமிழ் – முனைவர் இல. சுந்தரம் (விகடன் பிரசுரம்).
3. கீழடி – வைகை நதிக்கரையில் சங்ககால நகர நாகரிகம் (தொல்லியல் துறை வெளியீடு).
4. பொருநை – ஆற்றங்கரை நாகரிகம் (தொல்லியல் துறை வெளியீடு).
5. Social Life of Tamils (Dr.K.K.Pillay) A Joint publication of TNTB & ESC and RMRL – (in print).
6. Social Life of the Tamils – The Classical Period (Dr.S.Singaravelu) (Published by International institute of Tamil Studies.
7. Historical Heritage of the Tamils (Dr.S.V.Subramanian, Dr.K.D.Thirunavukkarasu)
8. The Contributions of the Tamils to Indian Culture (Dr.M.Valarmathi) (Published by International

institute of Tamil Studies)

9. Keeladi – 'Sangam City Civilzation on the vanks of river vaigai' (Jointly Published by Department of Archaeology & Tamil Nadu Text Book and Educational Services Corporation, Tamil Nadu)
10. Studies in the History of India with Special Reference to Tamil Nadu (Dr.K.K.Pillay) (Published by the author)
11. Porunai Civilization (Jointly Published by Department of Arcaeology & Tamil Nadu Text Book and Educational Services Corporation, Tamilnadu).
12. Journey of Civilization Indus to vaigai (R.Balakrishnan) (Published by RMRL) – Reference Book

GE20301	தமிழரும் தொழில்நுட்பமும்	L T P C 1 0 0 1

அலகு I நெசவு மற்றும் பானைத் தொழில்நுட்பம் 3

சங்க காலத்தில் நெசவுத் தொழில் - பானைத் தொழில்நுட்பம் - கருப்பு சிவப்பு பாண்டங்கள் - பாண்டங்களில் கீறல் குறியீடுகள்.

அலகு II வடுவமைப்பு மற்றும் கட்டிடத் தொழில்நுட்பம் 3

சங்க காலத்தில் வடிவமைப்பு மற்றும் கட்டுமானங்கள் &சங்க காலத்தில் வீட்டுப் பொருட்களில் வடிவமைப்பு - சங்க காலத்தில் கட்டுமான பொருட்களும் நடுகல்லும் - சிலப்பதிகாரத்தில் மேடை வடிவமைப்பு பற்றிய விவரங்கள் - மாமல்லபுரச் சிற்பங்களும், கோவில்களும் - சோழர் காலத்துப் பெருங்கோயில்கள் மற்றும் பிற வழிபாட்டுத் தலங்கள் - நாயக்கர் காலக் கோயில்கள் - மாதிரி கட்டமைப்புகள் பற்றி அறிதல், மீனாட்சி அம்மன் ஆலயம் மற்றும் திருமலை நாயக்கர் மஹால் - செட்டி நாட்டு வீடுகள் - பிரிட்டிஷ் காலத்தில் சென்னையில் இந்தோ - சாரோசெனிக் கட்டிடக்கலை.

அலகு III உற்பத்தி தொழில் நுட்பம் 3

கப்பல் கட்டும் கலை - உலோகவியல் - இரும்புத் தொழிற்சாலை - இரும்பு உருக்குதல், எஃகு - வரலாற்றுச் சின்னங்களாக செம்பு மற்றும் தங்க நாணயங்கள் - நாணயங்கள் அச்சடித்தல் - மணி உருவாக்கும் தொழிற்சாலைகள் - கல்மணிகள், கண்ணாடி மணிகள் - சுடுமண் மணிகள் - சங்கு மணிகள் -எலும்புத் துண்டுகள் - தொல்லியல் சான்றுகள் - சிலப்பதிகாரத்தில் மணிகளின் வகைகள்.

அலகு IV வேளாண்மை மற்றும் நீர்பாசனத் தொழில்நுட்பம் 3

அணை - ஏரி, குளங்கள், மதகு - சோழர்காலக் குமிழித் தூம்பின் முக்கியத்துவம் - கால்நடை பராமரிப்பு - கால்நடைகளுக்காக வடிவமைக்கப்பட்ட கிணறுகள் - வேளாண்மை மற்றும் வேளாண்மை சார்ந்த செயல்பாடுகள் - கடல்சார் அறிவு - மீன்வளம் - முத்து மற்றும் முத்துக்குளித்தல் - பெருங்கடல் குறித்த பண்டைய அறிவு -அறிவுசார் சமூகம்.

அலகு V *அறிவியல் தமிழ் மற்றும் கணித்தமிழ்* 3

அறிவியல் தமிழின் வளர்ச்சி -கணித்தமிழ் வளர்ச்சி - தமிழ் நூல்களை மின்பதிப்பு செய்தல் - தமிழ் மென்பொருட்கள் உருவாக்கம் - தமிழ் இணையக் கல்விக்கழகம் - தமிழ் மின் நூலகம் - இணையத்தில் தமிழ் அகராதிகள் - சொற்குவைத் திட்டம்.

TOTAL PERIODS: 15

TEXT CUM REFERENCE BOOKS:

1. தமிழக வரலாறு – மக்களும் பண்பாடும் – கே.கே.பிள்ளை. (வெளியீடு தமிழ்நாடு பாடநூல் மற்றும் கல்வியியல் பணிகள் கழகம்).
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3. கீழடி – வைகை நதிக்கரையில் சங்ககால நகர நாகரிகம் (தொல்லியல் துறை வெளியீடு).
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8. The Contributions of the Tamils to Indian Culture (Dr.M.Valarmathi) (Published by International institute of Tamil Studies)
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10. Studies in the History of India with Special Reference to Tamil Nadu (Dr.K.K.Pillay) (Published by the author)
11. Porunai Civilization (Jointly Published by Department of Arcaeology & Tamil Nadu Text Book and Educational Services Corporation, Tamilnadu).
12. Journey of Civilization Indus to vaigai (R.Balakrishnan) (Published by RMRL) – Reference Book

SEMESTER - III

MA20301 **TRANSFORMS AND BOUNDARY VALUE PROBLEMS** **3 1 0 4**

(COMMON TO ALL BRANCHES)

COURSE OBJECTIVES

To enable the students to

- introduce Fourier series analysis which is central to many applications in engineering apart from its use in solving boundary value problems.
- acquaint the students with Fourier transform techniques used in wide variety of situations in which the functions used are not periodic.
- formulate Partial Differential Equations and use Mathematical tools for the solution of PDE that model several physical processes.
- develop the modeling of one dimensional equation of heat conduction, wave equation and two dimensional Laplace equation.
- develop Z- transform techniques which will perform the same task for discrete time systems as Laplace Transform does for continuous systems, a valuable aid in analysis of continuous time systems.

UNIT I FOURIER SERIES 12

Dirichlet's conditions ; General Fourier series ; Odd and even functions ; Half range series ; Complex form of Fourier Series ; Parseval's identity ; Harmonic Analysis.

UNIT II FOURIER TRANSFORMS 12

Fourier integral theorem (without proof) ; Fourier transform pair ; Convolution theorem ; Parseval's identity ; Sine and Cosine transforms - Properties ; Transforms of elementary functions.

UNIT III PARTIAL DIFFERENTIAL EQUATIONS 12

Formation of partial differential equations ; Lagrange's linear equation ; Solutions of four standard types of first order partial differential equations ; Linear partial differential equations of second order with constant coefficients.

UNIT IV FOURIER SERIES SOLUTION TO PARTIAL DIFFERENTIAL EQUATIONS 12

Solutions of One-dimensional wave and heat equation; Steady state two-dimensional heat equation.

UNIT V Z - TRANSFORMS AND DIFFERENCE EQUATIONS 12

Z; transforms - Elementary properties ; Inverse Z; transform - Convolution theorem ; Formation of difference equations ; Solution of difference equations using Z; transform.

TOTAL PERIODS 60

COURSE OUTCOMES

At the end of the course, the students will be able to

- derive Fourier series, their possible forms of representations of periodic functions.
- identify and formulate a function in frequency domain whenever the function is defined in time domain.
- formulate and solve partial differential equations that occur in many engineering applications.
- model wave and heat equations, solve certain boundary value problems and use the solution methods in engineering applications.
- demonstrate the use of Z-transform to convert discrete functions into complex frequency domain representation.

TEXT BOOKS

1. Veerarajan T., "Transforms and Partial Differential Equations", Tata McGraw Hill Education Pvt. Ltd., New Delhi, Second reprint, 2012.
2. Grewal. B.S, "Higher Engineering Mathematics", 41st Edition, Khanna Publications, Delhi,(2011).

REFERENCES

1. Narayanan S., Manickavasagam Pillai.T.K and Ramanaiah.G "Advanced Mathematics for Engineering Students" ,Vol. II & III, S.Viswanathan Publishers Pvt Ltd. 1998.
2. Larry C. Andrews, Bhimsen K. Shivamoggi, "Integral Transforms for Enginears", SPIE Optical Engineering press, Washington USA (1999).
3. Ramana. B.V., "Higher Engineering Mathematics", Tata Mc-GrawHill Publishing Company limited, New Delhi (2010).
4. Glyn James, "Advanced Modern Engineering Mathematics", 3rd Edition, Pearson Education (2007).
5. Erwin Kreyszig., "Advanced Engineering Mathematics" 10th Edition,Wiley Publications.

CO-PO MAPPING:

Mapping of Course Outcomes with Programme Outcomes (3/2/1 indicates strength of correlation) 3-Strong,2-Medium,1-Weak														
COs	Programme Outcomes(POs)													
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	3	3	2	2	-	-	-	-	-	-	-	1	3	2
CO2	3	3	2	1	-	-	-	-	-	-	-	1	3	2
CO3	3	2	3	2	-	-	-	-	-	-	-	1	3	2
CO4	3	2	2	2	-	-	-	-	-	-	-	1	3	2
CO5	3	3	2	2	-	-	-	-	-	-	-	1	3	2

PAAVAI ENGINEERING COLLEGE, NAMAKKAL
Approved
BOARD OF STUDIES
Mathematics
14/8/21
AUTONOMOUS

EC20308 DIGITAL PRINCIPLES AND SYSTEM DESIGN 3 0 0 3

COURSE OBJECTIVES

To enable the students to

- understand the fundamentals and simplification of digital logic.
- design the various combinational logic circuits using logic gates.
- know the design procedures for synchronous sequential circuits.
- be familiar with the concepts of asynchronous sequential circuits.
- acquire basic knowledge about memory devices and HDL programming.

UNIT I BOOLEAN ALGEBRA AND LOGIC GATES 9

Boolean laws and theorems - De-Morgan's Theorem- Boolean functions - Canonical and Standard Forms - Sum of products and product of sums, Simplifications of Boolean functions using Karnaugh map and Quine McClusky method - Implementation of Boolean functions using logic gates - NAND - NOR implementations.

UNIT II COMBINATIONAL CIRCUITS 9

Design procedure of Combinational circuits - Adders, Subtractors, Carry look ahead adder, BCD adder, Multiplexer, De-multiplexer, Decoder, Encoder, Priority encoder, 2-bit Magnitude comparator; Code converters, Parity Generator and Checker.

UNIT III SEQUENTIAL CIRCUITS 9

Latches,Flip flops - SR, JK, D and T; Realization of flip flop using other flip flops; Asynchronous and Synchronous counters; Classification of sequential circuits - Moore and Mealy; Design of Synchronous counters - Modulo - N counter; Shift registers.

UNIT IV ASYNCHRONOUS SEQUENTIAL CIRCUITS 9

Design of fundamental mode circuits - Primitive flow table, Minimization of Primitive flow table, State assignment table, Excitation table; Cycles - Race Free State assignment; Hazards- Static, Dynamic, Essential Hazards, Hazards free elimination.

UNIT V MEMORY DEVICES AND INTRODUCTION TO HDL 9

Classification of memories - ROM organization, types; RAM - RAM organization, Write operation, Read operation, Static RAM Cell, Bipolar RAM cell, Dynamic RAM cell; Programmable Logic Devices - PLA, PAL; Introduction to HDL - HDL Models of Combinational circuits, HDL Models of Sequential Circuits.

TOTAL PERIODS 45

COURSE OUTCOMES

At the end of the course, the students will be able to

- apply the concept of Boolean algebra, K map and tabulation method in digital circuits.
- design and implement combinational circuits for various real time applications.
- construct sequential logic circuits using flipflops.
- analyze the asynchronous sequential circuits
- optimize the concepts of memory devices, PLD's and HDL programming.

TEXT BOOKS

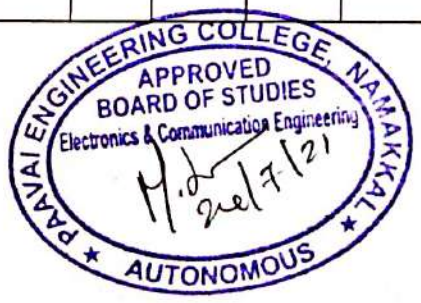
1. M. Morris Mano, "Digital Design", 3rd Edition, Prentice Hall of India Pvt. Ltd., New Delhi, 2003/Pearson Education (Singapore) Pvt. Ltd., New Delhi,2003.
2. H. Charles Roth Jr, "Digital System Design using VHDL", Thomson/ Brookscole, 2005.

REFERENCES

1. S. Salivahanan and S. Arivazhagan, "Digital Circuits and Design", 3rd Edition, Vikas Publishing House Pvt.Ltd, New Delhi, 2007.
2. John .M Yarbrough, "Digital Logic Applications and Design", Thomson Publications, New Delhi,2007.
3. Charles H.Roth, "Fundamentals of Logic Design", Thomson Publication Company, 2003.
4. Donald P.Leach and Albert Paul Malvino, "Digital Principles and Applications", 5th edition, Tata Mc-Graw Hill Publishing Company Limited, New Delhi, 2003.

CO-PO MAPPING :

Mapping of Course Outcomes with Programme Outcomes : (1,2,3 indicates the strength of correlation) 3 – Strong , 2 – Medium , 1 – Weak													
COs	Programme Outcomes (POs)												
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	3	3	3	2	-	-	-	-	2	-	-	3	3	3
CO2	3	3	3	2	-	-	-	-	2	-	-	-	3	3
CO3	3	3	3	-	-	-	-	-	-	-	-	-	3	3
CO4	3	3	3	2	-	-	-	-	-	-	-	3	3	3
CO5	3	3	3	2	-	-	-	-	2	-	-	3	3	3



IT20301 **OBJECT ORIENTED PROGRAMMING** 3 0 0 3

COURSE OBJECTIVES

To enable the students to

- understand the Object Oriented Programming concepts.
- study the concept of constructor and operator overloading.
- learn the basic concepts of inheritance and utilization.
- know the concepts of Java using Packages and Arrays.
- study of Interface and I/O streams.

UNIT I INTRODUCTION 9

Object-Oriented Paradigm - Elements of Object Oriented Programming, Merits and Demerits of OO Methodology; C++ fundamentals - Classes and Objects, Function, Function overloading, Static data and member functions, inline function.

UNIT II CONSTRUCTOR AND OPERATOR OVERLOADING 9

Constructor - Copy Constructors, and Default Arguments; Array of Objects - Pointer to Object member; Friend Function; Operator Overloading - binary and Unary operator overloading.

UNIT III TEMPLATE AND INHERITENCE 9

Templates - Function Template, Class Template; Inheritance - Derived class, Abstract class, Types of Inheritance; Virtual Functions; Exception Handling.

UNIT IV INTRODUCTION TO JAVA 9

Introduction to JAVA - bytecode, virtual machines, objects, classes, Javadoc, packages, Arrays, Strings.

UNIT V INHERITANCE, THREADING AND I/O 9

Inheritance; interfaces and inner classes; exception handling; threads; Streams and I/O.

TOTAL PERIODS 45

COURSE OUTCOMES

At the end this course, students will be able to

- utilize the principles of Object Oriented Programming in program.
- develop a program using Constructor.
- utilize the inheritance and polymorphism concepts for solutions to a given problems.
- develop simple Java program using class, methods and objects.
- implement the concepts of concurrent programming.

TEXT BOOKS

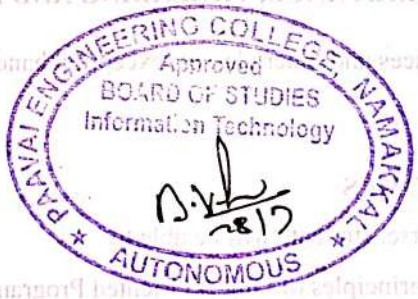
1. Herbert Schildt "C++: The Complete Reference", Tata McGraw Hill, 4th Edition, 2003.
2. Herbert Schildt, "JAVA, The Complete Reference" Tata Mcgraw Hill, 8th edition, 2011.

REFERENCES

1. Bjarne Stroustrup, "The C++ Programming Language", Addison Wesley, 2014.
2. K.R. Venugopal, Rajkumar Buyya, T.Ravishankar, "Mastering C++", TMH, 2009.
3. Bruce Eckel, "Thinking in JAVA", Prentice Hall,2006 6. Kathy Sierra, Bert Bates," Head First JAVA",O"Rielly,2005.
4. Kathy Sierra, Bert Bates," Head First JAVA",O"Rielly,2005.

CO-PO MAPPING:

Mapping of Course Outcomes with Programme Outcomes (3/2/1 indicates strength of correlation) 3-Strong,2-Medium,1-Weak														
COs	**Programme Outcomes(POs)**													
	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**
CO1	3	2	2	1	-	-	-	-	-	-	2	1	2	2
CO2	3	2	2	1	-	-	-	-	1	1	2	1	2	2
CO3	3	2	2	1	-	-	-	-	1	1	-	1	2	2
CO4	3	2	2	1	1	-	-	-	1	1	-	1	2	2
CO5	3	2	2	1	1	-	-	-	1	1	2	1	2	2



IT20302 DESIGN AND ANALYSIS OF ALGORITHMS 3 0 0 3

COURSE OBJECTIVES

To enable the students to

- learn the fundamental concepts and techniques for problem solving and algorithm design.
- study the importance of computational complexity of the algorithm.
- know the important algorithm design techniques.
- study the various techniques to solve the problem.
- understand the limitations of algorithmic power.

UNIT I INTRODUCTION 9

Notion of Algorithm; Fundamentals of Algorithmic Problem Solving; Important Problem Types; Fundamentals of the Analysis of Algorithm Efficiency - Analysis Framework, Asymptotic Notations and its Properties; Mathematical Analysis of Recursive and Non - Recursive Relations (Insertion sort, bubble sort, Selection sort, Towers of Hanoi).

UNIT II BRUTE FORCE AND DIVIDE-AND-CONQUER 9

Brute Force - Closest-Pair and Convex-Hull Problems; Exhaustive Search - Travelling Salesman Problem, Knapsack Problem, Assignment Problem; Divide and Conquer methodology - Merge sort, Quick sort, Binary Search, Multiplication of Large Integers, Strassen's Matrix Multiplication, Closest Pair Problem and Convex Hull Problem.

UNIT III DYNAMIC PROGRAMMING AND GREEDY TECHNIQUE 9

Dynamic Programming - Computing a Binomial Coefficient, Warshall's and Floyd's algorithm, Optimal Binary Search Trees, 0/1 Knapsack Problem and Memory functions; Greedy Technique - Prim's algorithm. Kruskal's algorithm, Dijikstra's algorithm, Huffman trees.

UNIT IV ITERATIVE IMPROVEMENT 9

The Simplex Method; The Maximum-Flow Problem; Maximum Matching in Bipartite Graphs; The Stable marriage Problem.

UNIT V ALGORITHM DESIGN TECHNIQUE AND ITS LIMITATIONS 9

Backtracking - n-Queen problem, Hamiltonian Circuit Problem, Subset Sum Problem; Branch and Bound - Assignment problem, Knapsack problem, Travelling Salesman Problem; Limitation of Algorithm Power - P, NP, NP Complete Problems, Approximation Algorithms for NP-hard Problems.

TOTAL PERIODS 45

COURSE OUTCOMES

At the end this course, students will be able to

- design algorithms for various computing problems.
- analyze the time and space complexity of algorithms.
- implement the various algorithms design techniques for different problems.
- analyze the different algorithm design techniques for a given problem.
- utilize the algorithm design techniques of NP complete and NP hard problems.

TEXT BOOKS

1. Anany Levitin, "Introduction to the Design and Analysis of Algorithms", Third Edition, Pearson Education, 2012.
2. Thomas H.Cormen, Charles E.Leiserson, Ronald L. Rivest and Clifford Stein, "Introduction to Algorithms", Third Edition, PHI Learning Private Limited, 2012.

REFERENCES

1. Alfred V. Aho, John E. Hopcroft and Jeffrey D. Ullman, "Data Structures and Algorithms", Pearson Education, Reprint 2006.
2. Donald E. Knuth, "The Art of Computer Programming", Volumes 1& 3 Pearson Education, 2009.
3. Steven S. Skiena, "The Algorithm Design Manual", Second Edition, Springer, 2008.
4. Harsh Bhasin, "Algorithms Design and Analysis", Oxford University Press, 2015.

CO-PO MAPPING:

Mapping of Course Outcomes with Programme Outcomes (3/2/1 indicates strength of correlation) 3-Strong,2-Medium,1-Weak														
COs	Programme Outcomes (POs)													
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	3	3	-	2	1	-	2	1	1	1	2	3	1	-
CO2	3	3	1	2	2	-	2	-	-	-	2	3	2	-
CO3	3	3	1	2	2	-	2	-	-	-	2	3	2	2
CO4	3	3	1	2	2	-	2	-	-	-	2	3	2	1
CO5	3	3	1	2	2	-	2	-	-	-	2	3	2	1

IT20303 COMPUTER ARCHITECTURE 3 0 0 3

COURSE OBJECTIVES

To enable students to

- learn the basic structure and operations of a computer.
- study the students with arithmetic and logic unit and implementation of fixed point and floating - point arithmetic unit.
- acquire knowledge about pipelining and parallel processing.
- understand the concept of virtual and cache memory.
- know the different ways of communicating with I/O devices and standard I/O interfaces.

UNIT I BASIC STRUCTURE OF COMPUTER SYSTEM 9

Functional Units, Basic Operational Concepts, Bus Structure, Performance, Instructions, Language of the Computer - Operations, Operands; Instruction representation, Logical operations, Execution of complete instruction, Addressing Modes.

UNIT II ARITHMETIC AND LOGIC UNIT 9

Addition and Subtraction; Multiplication; Division; Floating Point Representation - Floating Point Operations.

UNIT III PIPELINING AND PARALLEL PROCESSING 9

Pipelining Basic concepts - Data hazards, Instruction hazards, Structural Hazards; Influence on instruction sets; Data path and control considerations; Performance considerations; Exception handling; Parallel Processing Challenges; Flynn's classification - SISD, MIMD, SIMD, SPMD; Hardware multithreading.

UNIT IV MEMORY SYSTEM 9

Basic concepts - Semiconductor RAM, ROM; Cache memories - measuring and improving cache Performance; Virtual memory; Memory management requirements; Secondary storage devices.

UNIT V I/O ORGANIZATION 9

Accessing I/O devices - Programmed Input/ Output; Interrupts; Direct Memory Access; Buses; Interface circuits - Standard I/O Interfaces (PCI, SCSI, and USB); I/O devices and processors.

TOTAL PERIODS 45

COURSE OUTCOMES

At the end this course, students will be able to

- explain the basics structure and operation of a digital computer.
- utilize the operations of the arithmetic unit including the algorithms.

- implement the technique of pipelining and parallel processing.
- analyze the memory sub-systems of typical computer.
- develop the different ways of communication with I/O devices and standard I/O interfaces.

TEXT BOOKS

1. David A. Petterson and John L. Hennessey, "Computer organization and design', Morgan Kauffman / Elsevier, Fifth edition, 2014.

REFERENCES

1. William Stallings, Computer Organization and Architecture – Designing for Performance, Eighth Edition, Pearson Education, 2010.
2. John P. Hayes, Computer Architecture and Organization, Third Edition, Tata McGraw Hill, 2012.
3. Carl Hamacher, ZvonkoVranesic and SafwatZaky, "Computer Organization", Fifth Edition, Tata McGraw Hill, 2012.
4. Heuring V.P. and Jordan H.F., —Computer Systems Design and Architecturel, 2nd Edition, Pearson Education, New Delhi, 2004.

CO-PO MAPPING:

Mapping of Course Outcomes with Programme Outcomes (3/2/1 indicates strength of correlation) 3-Strong,2-Medium,1-Weak														
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CO1	3	3	2	2	-	-	-	-	-	-	-	2	2	2
CO2	3	2	2	3	2	-	-	-	-	-	-	2	2	2
CO3	3	2	1	2	2	-	-	-	-	2	-	2	-	2
CO4	3	3	2	3	2	-	-	-	-	2	-	3	-	2
CO5	2	3	2	3	2	-	-	-	-	2	-	3	2	2

MC20301 **VALUE EDUCATION** 2 0 0 0

COURSE OBJECTIVES

To enable the students to

- develop the individual multi-dimensionally in physical, intellectual, emotional and spiritual dimensions.
- facilitate individuals think about and reflect on different values.
- understand their responsibility in making choices and the practical implications of expressing them.
- instigate to choose their personal, social, moral and spiritual values.
- design and chisel the overall personality of an individual.

UNIT I PERSONAL VALUES 6

Value Education - Definition, Types of values; Human values - Respect, Acceptance, Consideration, Appreciation, Listening, Openness, Affection, Patience, Honesty, Forgiveness, Sacrifice, Authenticity, Self Control, Altruism, Tolerance and Understanding, Wisdom, Decision making, Self –actualization, Character formation towards positive Personality, Contentment; -Religious Values -Humility, Sympathy and Compassion, Gratitude. Peace, Justice, Freedom, Equality.

UNIT II COMMUNAL VALUES 6

Social Values - Pity and probity - Self control - Respect to - Age, Experience, Maturity, Family members, Neighbors - Universal Brotherhood - Flexibility -Peer pressure - Sensitization towards Gender Equality, Physically challenged, Intellectually challenged - Reliability - Unity - Modern Challenges of Adolescent Emotions and behavior - Comparison and Competition- Positive and Negative thoughts- Arrogance, Anger and Selfishness.

UNIT III ENGINEERING ETHICS 6

Professional Values - Knowledge thirst - Sincerity in profession- Regularity, Responsibility, Punctuality and Faith - Perseverance - Courage - Competence - Co-operation - Curbing unethical practices - Integrity, Social Consciousness and Responsibility. Global Values - Computer Ethics - Moral Leadership - Code of Conduct - Corporate Social Responsibility.

UNIT IV SPIRITUAL VALUES 6

Developing Spirituality - Thinking process, Moralization of Desires - Health benefits- Physical exercises - Mental peace - Meditation - Objectives, Types, Effects on body, mind and soul- Yoga - Objectives, Types, Asanas. Family values - family's structure, function, roles, beliefs, attitudes and ideals, Family Work Ethic, Family Time, Family Traditions.

UNIT V HUMAN RIGHTS 6

Classification of Human Rights - Right to Life, Liberty and Dignity- Right to Equality - Right against Exploitation - Cultural and Educational Rights- Physical assault and Sexual harassment - Domestic violence.

TOTAL PERIODS 30

COURSE OUTCOMES

At the end this course, students will be able to

- cultivate the values needed for peaceful living in the existing society.
- comprehend humanistic values to develop peace in the world.
- foster ethics in profession and usage of Technology.
- orient with the importance of value education towards personal, group and spiritual attributes.
- nurture physical, mental, spiritual growth to face the competitive world.

TEXT BOOKS

1. Little, William, An introduction of Ethics. Allied publisher, Indian Reprint 1955.
2. Sharma, S.P. Moral and Value Education; Principles and Practices, Kanishka publishers, 2013.

REFERENCES

1. "Values (Collection of Essays)". Sri Ramakrishna Math. Chennai. 1996.

CO-PO MAPPING:

Mapping of Course Outcomes with Programme Outcomes (3/2/1 indicates strength of correlation) 3-Strong,2-Medium,1-Weak														
COs	Programme Outcomes(POs)													
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	-	-	-	-	-	-	-	2	2	-	-	2	-	-
CO2	-	-	-	2	-	2	-	1	3	2	1	3	-	-
CO3	-	-	3	2	2	3	2	3	3	1	3	3	-	-
CO4	-	-	3	1	-	2	-	-	1	-	-	3	-	-
CO5	-	-	-	-	-	1	-	-	-	-	-	3	-	-

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EC20309 **DIGITAL LABORATORY** 0 0 4 2

COURSE OBJECTIVES

To enable the students to

- understand the concept of Boolean theorems.
- know the concept of combinational circuits using digital logic gates.
- design and implement the combinational and sequential logic circuits using MSI devices
- acquire knowledge about simulation of digital circuits with Verilog HDL.

LIST OF EXPERIMENTS

1. Verification of Boolean theorems using digital logic gates
2. Design and implementation of Full and Half adders and Full and Half subtractors using logic gates.
3. Design and implementation of code converter: Binary to Gray code and Gray code to Binary code.
4. Design and implementation of 2-bit magnitude comparator.
5. Design and implementation of 4-bit binary adder / subtractor using IC7483
6. Design and implementation of encoder and decoder using basic gates.
7. Design and implementation of multiplexers and Demultiplexers using basic gates.
8. Design and implementation of Shift registers
9. Design and implementation of 3-bit synchronous up (or) down counter.
10. Design and Simulation of Full and Half Adders, Full and Half Subtractors.

TOTAL PERIODS 60

COURSE OUTCOMES

At the end of the course, the students will be able to

- design adders and subtractors using basic logic gates and Karnaugh map.
- create code converters using basic logic gates.
- implement the combinational logic circuits like MUX, DEMUX, Encoder, Decoder etc.
- design various counters and shift registers and simulate digital circuits using Verilog HDL.

CO-PO MAPPING :

Mapping of Course Outcomes with Programme Outcomes : (1,2,3 indicates the strength of correlation) 3 – Strong , 2 – Medium , 1 – Weak														
COs	**Programme Outcomes (POs)**													
	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**
CO1	3	3	3	2	-	-	-	-	2	-	-	3	3	3
CO2	3	3	3	2	-	-	-	-	2	-	-	-	3	3
CO3	3	3	3	-	-	-	-	-	-	-	-	-	3	3
CO4	3	3	3	2	-	-	-	-	-	-	-	3	3	3

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IT20306 OBJECT ORIENTED PROGRAMMING LABORATORY 0 0 4 2

COURSE OBJECTIVES

To enable students to

- understand Object Oriented Programming concepts.
- learn the various features of OOP in C++.
- acquire knowledge in concepts of reusability, platform independence in JAVA.
- study the thread based concepts in JAVA.

LIST OF EXPERIMENTS

1 Creation of classes and use of different types of functions.

2 Programs using Constructor and Destructor

3 Count the number of objects created for a class using static member function.

4 Write programs using function overloading and operator overloading.

5 Programs using virtual function and friend functions.

6 Implementation of user defined function using exception handling mechanism.

7 Programs using function templates and class templates.

8 Develop Simple program in Java using class, objects and methods.

9 Programs for Method Overloading and Method Overriding.

10 Programs using inheritance.

11 Program to implement Inheritance, Interfaces and Packages

12 Create an thread in java using Thread and Runnable Interface.

TOTAL PERIODS 60

COURSE OUTCOMES

At the end this course, students will be able to

- apply class components that protect data integrity and produce classes that are re-usable and maintainable.
- analyze the C++ programs for errors and exceptions.
- develop the applications using object oriented concepts.
- design simple Java program using class, methods and objects.

LAB EQUIPMENT FOR A BATCH OF 30 STUDENTS

SOFTWARE: Turbo C++, Jdk

HARDWARE: Standalone desktops 60 Nos.

CO-PO MAPPING:

Mapping of Course Outcomes with Programme Outcomes (3/2/1 indicates strength of correlation) 3-Strong,2-Medium,1-Weak														
COs	Programme Outcomes(POs)													
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	3	2	-	2	2	-	-	-	-	-	-	-	3	3
CO2	2	1	-	2	2	-	-	-	-	-	-	-	1	3
CO3	3	2	-	2	3	-	-	-	-	-	-	-	-	3
CO4	3	-	-	2	-	-	-	-	-	-	-	-	-	3

EN20301 ENGLISH PROFICIENCY COURSE LABORATORY 0 0 2 1

COURSE OBJECTIVES

To enable students to

- familiarize with the reading skills such as skimming and scanning.
- practise writing tasks to the level expected.
- develop listening strategies such as listening for key words, making inferences and identifying main ideas.
- speak well without inhibition and to assist the students in improving their vocabulary, pronunciation and comprehension of grammar.

EXERCISES FOR PRACTICE

1. Listening Exercises from TOEFL
    a. Conversations, Lectures
2. Listening Exercises from IELTS
    a. Places and directions
    b. Actions and processes
3. Reading Exercises from PTE
    a. Re-order paragraphs
4. Reading Exercises from IELTS
    a. Opinions and attitudes
    b. Locating and matching information
    c. Identifying information
5. Reading Exercises from BEC Vantage & BEC Higher
    a. Error identification
    b. Gap filling
6. Writing Exercises from PTE
    a. Summarize written text
7. Writing Exercises from IELTS
    a. Describing maps
    b. Describing diagrams
8. Speaking IELTS format
    a. Talking about familiar topics
    b. Giving a talk
    c. Discussion on a Topic

TOTAL PERIODS 30

COURSE OUTCOMES

At the end of this course, the students will be able to

- skim, scan and infer the given texts and attend the tasks successfully.
- write coherently using appropriate vocabulary and grammar.
- listen to speeches and conversations and answer the questions.
- communicate fluently and effectively on any given topics and appear with confidence for on-line tests. .

TEXT BOOKS

1. Cambridge IELTS 12 Academic Student's Book with Answers: Authentic Examination Papers (IELTS... by Cambridge University Press. New Delhi.2016
2. TOEFL iBT Prep Plus 2018-2019 4 Practice Tests) Kaplan Publishing. Newyork.2017.

REFERENCES

1. Cambridge University Press India Pvt. Ltd, New Delhi.2016.
2. PTE Academic Test builder. Macmillan Education. London. 2012.

CO-PO MAPPING:

Mapping of Course Outcomes with Programme Outcomes (3/2/1 indicates strength of correlation) 3-Strong,2-Medium,1-Weak														
COs	Programme Outcomes(POs)													
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CO1	-	-	-	2	3	-	-	-	3	2	-	-	-	-
CO2	-	-	2	2	-	-	1	1	3	2	-	2	-	-
CO3	-	-	-	-	-	3	1	2	3	2	2	3	-	-
CO4	-	-	-	-	-	2	2	3	3	2	2	1	-	-

SEMESTER - IV

MA20403 **PROBABILITY AND STATISTICS** **3 1 0 4**

COURSE OBJECTIVES

To enable the students to

- analyse the concept of Random variables and probability distribution in designing processes.
- know and differentiate the discrete and continuous two dimensional random variables.
- determine the concepts of hypotheses testing, its need and applications.
- equip with statistical techniques for designing experiments, analyzing, interpreting and presenting research data.
- emphasize the aspects of statistical tools in engineering problems.

UNIT I RANDOM VARIABLES 12

Discrete and continuous random variables; Moments - Moment generating functions; Binomial, Poisson, Geometric, Uniform, Exponential, Gamma and Normal distributions.

UNIT II TWO - DIMENSIONAL RANDOM VARIABLES 12

Functions of random variables - Joint distributions, Marginal and conditional distributions; Covariance - Correlation and Linear regression; Transformation of random variables; Applications of Central limit theorem (for independent and identically distributed random variables).

UNIT III TESTING OF HYPOTHESIS 12

Sampling distributions; Estimation of parameters; Statistical hypothesis - Large sample test for single mean and difference of means; Small samples - Tests based on t, Chi-square and F distributions for mean, variance and proportion; Contingency table (test for independent); Goodness of fit.

UNIT IV DESIGN OF EXPERIMENTS 12

ANOVA; One way and Two way classifications; Completely randomized design; Randomized block design; Latin square design; 2^2 factorial design.

UNIT V STATISTICAL QUALITY CONTROL 12

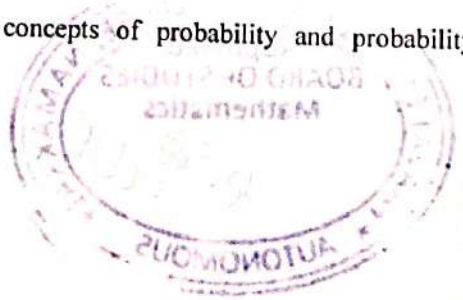
Control charts for measurements (X and R charts); Control charts for attributes (P, C and NP charts); Tolerance limits - Acceptance sampling.

TOTAL PERIODS 60

COURSE OUTCOMES

At the end of the course, the students will be able to

- demonstrate the fundamental concepts of probability and probability distributions of random variables in designing process.



- identify the differences in two dimensional random variables.
- implement the statistical techniques to hypotheses testing of engineering and management problems.
- be aware of the principles to be adopted for designing the experiments.
- compare statistical date using control chart in quality control.

TEXT BOOKS

1. Milton. J. S. and Arnold. J.C., "Introduction to Probability and Statistics", Tata McGraw Hill, 4th Edition, 2007.
2. Johnson. R.A. and Gupta. C.B., Miller and Freund's Probability and Statistics for Engineers", Pearson Education, Asia, 7th Edition, 2007.
3. Papoulis. A and Unnikrishnapillai.S., "Probability, Random Variables and Stochastic Processes" Mc-Graw Hill Education India,4th Edition, New Delhi, 2010.

REFERENCES

1. Devore. J.L., "Probability and Statistics for Engineering and the Sciences", Cengage Learning, New Delhi, 8th Edition, 2012.
2. Walpole. R.E., Myers. R.H., Myers. S.L. and Ye. K., "Probability and Statistics for Engineers and Scientists", Pearson Education, Asia, 8th Edition, 2007.
3. Ross, S.M., "Introduction to Probability and Statistics for Engineers and Scientists", 3rd Edition, Elsevier, 2004.
4. Spiegel. M.R., Schiller. J. and Srinivasan. R.A., "Schaum's Outline of Theory and Problems of Probability and Statistics", Tata McGraw Hill Edition, 2004.

CO-PO MAPPING:

Mapping of Course Outcomes with Programme Outcomes (3/2/1 indicates strength of correlation) 3-Strong,2-Medium,1-Weak														
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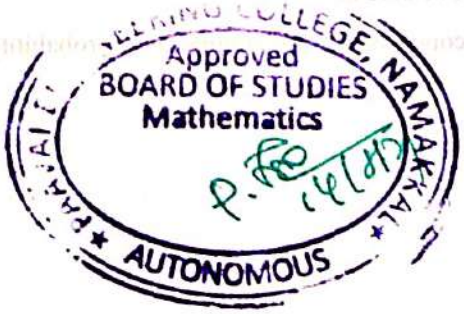
CO3	3	3	3	2	-	-	-	-	-	-	-	3	3	3
CO4	3	3	3	2	-	-	-	-	-	-	-	2	3	3
CO5	3	3	3	3	-	-	-	-	-	-	-	3	3	3



EC20407 MICROPROCESSORS AND MICROCONTROLLERS 3 0 0 3

COURSE OBJECTIVES

To enable the students to

- know the architecture and programming of 8085 and 8086 microprocessors.
- have a knowledge about signals and bus structure of 8086 microprocessor.
- learn the design aspects of I/O and memory interfacing circuits.
- acquire knowledge about the architecture of 8051 and PIC microcontroller.
- be familiar with the concepts of ARM and Pentium processors.

UNIT I MICROPROCESSORS 9

8085 Microprocessor - Architecture, Addressing modes, Instruction set; 8086 Microprocessor - Architecture, Addressing modes, Instruction set, assembler directives; Simple programs - 8085,8086; Stacks, Macros, Interrupts and interrupt service routines.

UNIT II 8086 BUS ARCHITECTURE 9

8086 signals, Basic configurations, System bus timing, System design using 8086, I/O programming; Introduction to Multiprogramming - System Bus Structure, Multiprocessor configurations, Coprocessor, Closely coupled and loosely Coupled configurations.

UNIT III 8086 I/O INTERFACING 9

Programmable Peripheral Interface; I/O interfacing - Serial communication interface, D/A and A/D Interface, Keyboard /display controller, Interrupt controller; Memory Interfacing - DMA controller.

UNIT IV MICROCONTROLLERS 9

8051 - Architecture, Signals, Special Function Registers (SFRs), I/O Ports, Memory, Interrupts, Addressing Modes, Instruction set, Assembly language programming; Introduction to PIC microcontroller.

UNIT V SYSTEM DESIGN USING MICROCONTROLLER 9

Case studies - Traffic light control, Washing machine control, Stepper Motor, Keyboard Interfacing, Sensor Interfacing; Overview of Pentium Processors - ARM Processors, Introduction to ARM Architecture.

TOTAL PERIODS 45

COURSE OUTCOMES

At the end of the course, the students will be able to

- infer the operations of microprocessors architecture.

- explain the multiprocessor configurations.
- design various I/O interfacing circuits.
- write programs using 8051 and PIC microcontrollers.
- devise various applications of microcontrollers.

TEXT BOOKS

1. Ramesh Gaonkar, "Microprocessor Architecture ,Programming and Applications with 8085",Penram International Publishing reprint,6th edition,2017.
2. Krishna Kant, "Microprocessors and Microcontrollers Architecture, programming and system design using 8085, 8086, 8051 and 8096". PHI 2007

REFERENCES

1. Doughlas V.Hall, "Microprocessors and Interfacing, Programming and Hardware", TMH,2012
2. A.K.Ray& K.M Bhurchandi, "Advanced Microprocessor and Peripherals – Architecture, Programming and Interfacing", Tata McGraw Hill, 2006.
3. Barry B Brey, "Intel Microprocessors: 8086/8088, 80286, 80386, 80486, Pentium, Pentium Pro Processors, Pentium II, Pentium III and Pentium 4: Architecture, Programming and Interfacing", Pearson Education, New Delhi, 2009.
4. Steve Furber "ARM System-On-chip Architecture", Pearson Education Limited, USA,2010.

CO-PO MAPPING :

Mapping of Course Outcomes with Programme Outcomes :

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COs	Programme Outcomes (POs)													
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CO2	3	2	2	2	2	-	-	-	-	-	-	-	3	3
CO3	3	2	2	2	2	2	2	-	-	-	-	-	3	3
CO4	3	2	2	2	2	-	-	-	-	-	-	-	3	3
CO5	3	2	2	2	2	2	2	-	-	-	-	-	3	3

IT20401 **SOFTWARE ENGINEERING** 3 0 0 3

COURSE OBJECTIVES

To enable the students to

- learn the Software life cycle models and system engineering process for developing a system from scratch.
- understand fundamental concepts of requirement engineering.
- acquire knowledge about Design levels of software engineering.
- learn the various software testing methods.
- study the software project management concepts.

UNIT I SOFTWARE PROCESS 9

The Evolving role of Software, Software, The changing Nature of Software, Legacy Software, A generic view of process, A layered Technology, A Process Framework, The Capability Maturity Model Integration (CMMI), Process Assessment, Personal and Team Process Models, Product and Process; Process Models - The Waterfall Model, Incremental Process Models, Incremental Model, The RAD Model, Evolutionary Process Models, Prototyping, The Spiral Model, The Concurrent Development Model, Specialized Process Models; the Unified Process.

UNIT II SOFTWARE REQUIREMENTS AND ANALYSIS 9

Requirements Engineering, Requirements Engineering tasks, Initiating the requirements Engineering Process, Eliciting Requirements, Developing Use cases, Building the Analysis Models, Elements of the Analysis Model, Analysis pattern, Negotiating Requirements, Validating Requirements; Requirements Analysis - Analysis Modeling approaches, data modeling concepts, Object oriented Analysis, Scenario based modeling, Flow oriented Modeling, Class based modeling, creating a behavior model.

UNIT III SOFTWARE DESIGN 9

Design Engineering, Design process, Design Quality, Design model, Agile Methods, Extreme Programming, Rapid Application development, Software Prototyping, Software Reuse, Application Frameworks, Application System Reuse, Software Evolution Program Evolution Dynamics, Software Maintenance; Evolution Processes - Legacy system evolution Planning; Verification and Validation ; Software Inspections; Automated Static analysis; Verification and Formal -methods.

UNIT IV SOFTWARE TESTING AND IMPLEMENTATION 9

Software testing fundamentals - Internal and external views of Testing, white box testing, basis path testing, control structure testing, black box testing, Regression Testing, Unit Testing, Integration Testing, Validation Testing, System Testing and Debugging.

UNIT V SOFTWARE PROJECT MANAGEMENT 9

Software Cost Estimation – Productivity, Estimation Techniques, Algorithmic Cost Modeling, Project Duration and Staffing; Process and Product Quality - Quality Assurance and Standards, Planning, Control, Software Measurement and Metrics; Process Improvement - Process Classification, Measurement, Analysis and Modeling, Change; Configuration Management - Planning Change Management, Version and Release Management, System Building.

TOTAL PERIODS 45

COURSE OUTCOMES

At the end this course, students will be able to

- analyze the different process models.
- apply the Requirement engineering process with emphasis on elicitation analysis and modelling for any given software requirement.
- implement the different methods for the design of a software system.
- utilize the software testing methods.
- create and maintain documentation for software engineering process.

TEXT BOOKS

1. Rogers. Pressman, "Software Engineering: A Practitioner's Approach", Mc-Graw Hill International, Eighth edition, 2015. (UNIT-I, II, IV).
2. Ian Sommerville, Software Engineering, 9th Edition, Pearson Education,2011. (UNIT-III, V).

REFERENCES

1. Richard E. Fairley, "Principles of Software Engineering", IEEE computer society press, 2010.
2. PankajJalote, "Software Engineering, A Precise Approach", Wiley India, 2010.
3. Kelkar S.A., "Software Engineering", Prentice Hall of India Pvt Ltd, 2007.
4. Shari P fleeger, Joanne Atlee, "Software Engineering: Theory and Practice", Fourth Edition, Pearson Education, 2010.

CO-PO MAPPING:

Mapping of Course Outcomes with Programme Outcomes (3/2/1 indicates strength of correlation) 3-Strong,2-Medium,1-Weak														
COs	Programme Outcomes(POs)													
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	3	2	3	-	1	-	-	-	3	2	-	2	2	1
CO2	3	2	3	-	1	-	-	-	2	2	-	3	1	2
CO3	2	3	3	-	3	-	-	-	1	-	1	2	2	1
CO4	2	3	3	-	3	-	-	-	1	-	2	2	2	1
CO5	3	3	-	-	-	-	-	-	3	3	2	3	1	3

IT20402 PYTHON PROGRAMMING 3 0 0 3

COURSE OBJECTIVES

To enable students to

- know the basics of algorithmic problem solving.
- study the python program logics.
- acquire knowledge in writing programs with condition and loops.
- understand the concepts of lists, tuple and dictionaries.
- acquire knowledge about file and modules.

UNIT I ALGORITHMS AND PROBLEM SOLVING 9

Algorithms - building blocks of algorithms (statements, state, control flow, functions); notation (pseudo code, flow chart, programming language); algorithmic problem solving- simple strategies for developing algorithms (iteration, recursion).

UNIT II EXPRESSION AND STATEMENTS 9

Python interpreter and interactive mode - values and types, int, float, boolean, string and list; variables, expressions, statements, tuple assignment, precedence of operators, comments; modules and functions - function definition and use, flow of execution, parameters and arguments.

UNIT III CONTROL AND FLOW FUNCTIONS 9

Conditional - Boolean values and operators, conditional (if), alternative (if-else), chained conditional (if-elif-else); Iteration - state, while, for, break, continue, pass; Fruitful functions - return values, parameters, local and global scope, function composition, recursion; Strings - string slices, immutability, string functions and methods, string module; Lists as array.

UNIT IV LISTS, TUPLES AND DICTIONARIES 9

Lists - list operations, list slices, list methods, list loop, mutability, aliasing, cloning lists, list parameters; Tuples - tuple assignment, tuple as return value; Dictionaries - operations and methods; advanced list processing - list comprehension; Illustrative programs - selection sort, insertion sort, merge sort, histogram.

UNIT V FILES AND PACKAGES 9

Files and exception - text files, reading and writing files, format operator; command line arguments - errors and exceptions; handling exceptions; modules; packages; Illustrative programs - word count, copy file.

TOTAL PERIODS 45

COURSE OUTCOMES

At the end of this course, students will be able to

- develop algorithmic solutions to simple computational problems.
- develop a simple Python programs.
- create simple Python programs with different logics.

- implement the compound data using Python lists, tuples, dictionaries.
- evaluate a Python program into files and Packages.

TEXT BOOKS

1. Allen B. Downey, ``Think Python: How to Think Like a Computer Scientist``, 2nd edition, Updated for Python 3, Shroff/O'Reilly Publishers, 2016. (http://greenteapress.com/wp/think-python/).
2. Martin C. Brown, —PYTHON:The Complete Referencel, McGraw-Hill, 2001.

REFERENCES

1. John V Guttag, —" Introduction to Computation and Programming Using Python", Revised and expanded Edition, MIT Press, 2013.
2. Robert Sedgewick, Kevin Wayne, Robert Dondero, —Introduction to Programming in Python: An Inter-disciplinary Approach, Pearson India Education Services Pvt. Ltd., 2016.
3. Guido van Rossum and Fred L. Drake Jr, —An Introduction to Python – Revised and updated for Python 3.2, Network Theory Ltd., 2011.
4. Timothy A. Budd, —Exploring Pythonl, Mc-Graw Hill Education (India) Private Ltd., 2015.

CO-PO MAPPING:

Mapping of Course Outcomes with Programme Outcomes (3/2/1 indicates strength of correlation) 3-Strong,2-Medium,1-Weak														
COs	Programme Outcomes(POs)													
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	3	2	1	-	-	1	-	-	-	-	-	1	2	-
CO2	3	2	1	-	2	1	-	-	-	-	-	1	2	-
CO3	3	2	1	-	2	1	-	-	-	-	-	1	2	-
CO4	3	2	1	-	2	1	-	-	-	-	-	1	2	-
CO5	3	2	1	1	2	1	-	-	-	-	-	1	2	-

IT20403 **OPERATING SYSTEMS** 3 0 0 3

COURSE OBJECTIVES

To enable students to

- understand the basic concepts and functions of operating systems.
- acquire knowledge about processes, threads, scheduling algorithms and concept of deadlocks.
- analyze various memory management schemes.
- learn file system interfaces and implementation process.
- study I/O streams and Mass storage management.

UNIT I INTRODUCTION TO OPERATING SYSTEMS 9

Introduction - Computer system organization, Operating Systems and types, Operating system structures, Services, System calls, System programs; Processes - Process concept, Process scheduling, Operations on Processes, Cooperating processes, Inter process communication; Threads - Overview, Multi-threading models - Threading issues.

UNIT II PROCESS MANAGEMENT AND DEADLOCK 10

CPU Scheduling - Concepts-scheduling criteria, Scheduling algorithms, Algorithm Evaluation; Process Synchronization - The critical-section problem, Synchronization hardware; Semaphores, Classic problems of synchronization - Monitors; Deadlock-System model - Deadlock Characterization, Methods for handling deadlocks, Deadlock prevention, Deadlock avoidance, Deadlock detection, Recovery from deadlock.

UNIT III MEMORY MANAGEMENT 9

Main Memory – Background, Swapping, Contiguous memory allocation, Paging Segmentation, Segmentation with paging; Virtual Memory – Background, Demand paging, Page replacement, Allocation of frames, Thrashing.

UNIT IV FILE SYSTEMS 9

File-System Interface - File concept, Access methods, Directory structure, File system mounting, File sharing, Protection; File-System Implementation - Directory implementation, Allocation methods, Free-space management, efficiency and performance, recovery, Network file systems.

UNIT V I/O SYSTEMS AND MASS STORAGE MANAGEMENT 8

I/O Systems - I/O Hardware, Application I/O interface, kernel I/O subsystem, streams, Performance; Mass - Storage Structure; Disk attachment - Disk scheduling, Disk management, Storage Device Management, Swap-space management, RAID, stable storage; **CASE STUDY:** LINUX system and Mobile OS-iOS and Android.

TOTAL PERIODS 45

COURSE OUTCOMES

At the end of this course, students will be able to

- evaluate various scheduling algorithms.
- analyze deadlock, prevention and avoidance algorithms.
- utilize the various memory management schemes.
- explain the functionality of file systems.
- analyze the administrative skills on Linux and windows servers.

TEXT BOOKS

1. Silberschatz, Galvin, and Gagne, "Operating System Concepts", Tenth Edition, Wiley India Pvt Ltd, 2018.
2. William Stallings, "Operating Systems – internals and design principles", Prentice Hall, 7th Edition, 2011.

REFERENCES

1. Andrew S. Tanenbaum, "Modern Operating Systems", Fourth Edition, Pearson Education, 2014.
2. Harvey M. Deital, "Operating Systems", Third Edition, Pearson Education, 2007.
3. Andrew S. Tannenbaum & Albert S. Woodhull, "Operating System Design and Implementation". Prentice Hall, 3rd Edition, 2006.
4. Gary J.Nutt, "Operating Systems", Pearson/Addison Wesley, 3rd Edition, 2004.

CO-PO MAPPING:

Mapping of Course Outcomes with Programme Outcomes (3/2/1 indicates strength of correlation) 3-Strong,2-Medium,1-Weak													
COs	Programme Outcomes(POs)												
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	1	3	-	-		-	-	-	1	-	-	-	2	1
CO2	3	3	3	-	3	-	-	-	1	-	-	1	2	1
CO3	2	3	3	-	3	-	-	-	1	-	1	2	2	1
CO4	2	3	3	-	3	-	-	-	1	-	2	3	2	1
CO5	3	3	-	-	-	-	-	-	3	3	2	3	1	3

EC20408	MICROPROCESSORS AND MICROCONTROLLERS LABORATORY	0	0	4	2

COURSE OBJECTIVES

To enable the students to

- implement the assembly language programming of 8085,8086 and 8051.
- experiment the interface concepts of various peripheral devices with the processor.
- understand the basic idea about the data transfer schemes and its applications.
- develop skills in simple program writing for 8085,8086, 8051 and its applications.

Assembly Language programming using 8085/8086/MASM

1. Programs using Arithmetic and logic operations.
2. Ascending / Descending order, Largest / Smallest of numbers.
3. String manipulations
4. Move a data block without overlap
5. Hexadecimal / ASCII / BCD code conversion.

Interfacing with 8086 Microprocessor

6. Stepper motor control.
7. Key board and Display.
8. Serial interface
9. Parallel interface

Programming using 8051 Microcontroller

10. Basic arithmetic and Logical operations.
11. ADC and DAC interfacing.

TOTAL PERIODS 60

COURSE OUTCOMES:

At the end of the course, students will be able to

- write assembly language programs for various applications.
- interface different peripherals with microprocessor.
- execute programs for different data transfer techniques using 8086.
- execute programs using 8051 and MASM.

CO-PO MAPPING :

Mapping of Course Outcomes with Programme Outcomes : (1,2,3 indicates the strength of correlation) 3 – Strong , 2 – Medium , 1 – Weak														
COs	**Programme Outcomes (POs)**													
	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**
CO1	3	2	2	2	2	-	-	-	-	-	-	-	3	3
CO2	3	2	2	2	2	-	-	-	-	-	-	-	3	3
CO3	3	2	2	2	2	2	2	-	-	-	-	-	3	3
CO4	3	2	2	2	2	-	-	-	-	-	-	-	3	3

PAAVAI ENGINEERING COLLEGE, NAMAKKAL
APPROVED
BOARD OF STUDIES
Electronics & Communication Engineering
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AUTONOMOUS

IT20405	PYTHON PROGRAMMING LABORATORY	0	0	4	2

COURSE OBJECTIVES

To enable the students to

- acquire programming skills in core python concepts.
- study about object oriented skills in python.
- create the skill of designing graphical user interfaces in python.
- study the database applications in python.

LIST OF EXPERIMENTS

1. Programs that take command line arguments (word count)
2. Find the most frequent words in a text read from a file
3. Simulate elliptical orbits in Pygam
4. Simulate bouncing ball using Pygame
5. Compute the GCD of two numbers
6. Find the square root of a number (Newtons method)
7. Exponentiation (power of a number)
8. Find the maximum of a list of numbers
9. Linear Search
10. Binary Search
11. Selection Sort
12. Merge Sort
13. Insertion Sort
14. First n prime numbers
15. Multiply matrices

TOTAL PERIODS 60

COURSE OUTCOMES

At the end this course, students will be able to

- develop solutions to simple computational problems using Python programs.
- solve problems using conditionals and loops in Python.
- create Python programs by defining functions and calling them.
- use Python lists, tuples and dictionaries for representing compound data.

LAB EQUIPMENT FOR A BATCH OF 30 STUDENTS

SOFTWARE: Python 3 interpreter for Windows/Linux.

HARDWARE: Standalone desktops 60 Nos.

CO-PO MAPPING:

Mapping of Course Outcomes with Programme Outcomes (3/2/1 indicates strength of correlation) 3-Strong,2-Medium,1-Weak														
COs	**Programme Outcomes(POs)**													
	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**
CO1	2	3	-	-	3	-	-	-	1	-	-	-	1	2
CO2	2	3	3	-	3	-	-	-	1	-	-	-	1	2
CO3	2	3	-	-	3	-	-	-	1	-	-	-	2	1
CO4	3	3	3	-	3	-	-	-	3	-	3	-	2	3

IT20406 OPERATING SYSTEMS LABORATORY 0 0 4 2

COURSE OBJECTIVES

To enable the students to

- understand shell programming and the use of filters in the UNIX environment.
- study the system calls and to process creation and inter process communication.
- learn to use the file system related system calls.
- learn the various CPU Scheduling Algorithms, page replacement algorithms and Deadlock avoidance.

LIST OF EXPERIMENTS

1. Basics of UNIX commands.
2. Shell Programming.
3. Implement the following CPU scheduling algorithms.
   a) FCFS b) SJF c)Priority d) Round Robin
4. Implement the following file allocation strategies.
   a) Sequential b) Indexed c) Linked
5. Implement Semaphores.
6. Implement Bankers Algorithm for Dead Lock Avoidance.
7. Implement an Algorithm for Dead Lock Detection.
8. Implement the following page replacement algorithms
   a) FIFO b) LRU c) Optimal
9. Implement Paging Technique of memory management.
10. Implement Shared memory and IPC.

TOTAL PERIODS 60

COURSE OUTCOMES

At the end this course, students will be able to

- implement deadlock avoidance, and Detection Algorithms.
- analyze the performance of various CPU Scheduling Algorithm.
- evaluate the performance of the various page replacement algorithms.
- analyze various IPC techniques in the operating system.

LAB EQUIPMENT FOR A BATCH OF 30 STUDENTS

SOFTWARE: Turbo C, Linux.

HARDWARE: Standalone desktops 60 Nos.

CO-PO MAPPING:

Mapping of Course Outcomes with Programme Outcomes (3/2/1 indicates strength of correlation) 3-Strong,2-Medium,1-Weak													
COs	Programme Outcomes(POs)												
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	3	3	1	1	1	-	-	-	1	-	-	-	2	1
CO2	3	3	3	1	3	-	-	-	1	-	-	1	2	1
CO3	3	3	3	1	3	-	-	-	1	-	1	2	2	1
CO4	3	3	3	1	3	-	-	-	1	-	2	3	2	1